

PATENT APPLICATION

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FORM PTO-1449	ATTY. DOCKET NO. PD-10981967-2	SERIAL NO. 09/699,080
APPLICANT Philip J. Kuekes, et al.		
NOV 23 2004	FILING DATE 3/29/99	GROUP

REFERENCE DESIGNATION OF U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
WSL	5665618	09/1997	Meyer, et al	438/172	
WSL	5646418	07/1997	Frazier et al	257/4	

FOREIGN PATENT DOCUMENTS

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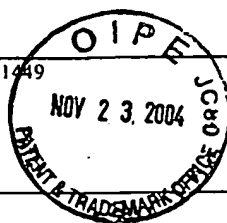
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

WSL	L. Guo et al., "Nanoscale silicon field effect transistors fabricated using imprint lithography," <u>Applied Physics Letters</u> , Vol. 71, pp. 1881-1883 (29 September 1997)
	L. Guo et al., "A Single-Electron Transistor Memory Operating at Room Temperature," <u>Science</u> , Vol. 2756, pp. 649-651 (31, January 1997)
	S.J. Tans et al., "Room-temperature transistor based on a single carbon nanotube," <u>Nature</u> , Vol. 393, pp. 49-52 (7 May 1998)
	K.K. Likharev, "Correlated discrete transfer of single electrons in ultrasmall tunnel junctions", <u>IBM Journal of Research and development</u> , Vol. 32, pp. 144-158 (January 1998)

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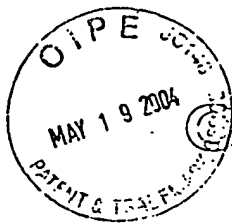
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

WSL	Heath, "A defect-Tolerant Computer Architecture: Opportunities for Nanotechnology", <u>Science</u> , Vol. 280, pp. 1716-1721 (June 12, 1998)

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LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
WSL	1A	3,975,623	8/17/76	Weinberger	235	152
	1B	4,208,728	6/17/80	Blahut et al	365	154
	1C	5,475,341	12/12/95	Reed	327	566
	1D	5,729,752	3/17/98	Snider et al	395	800
	1E	5,790,771	8/4/98	Culbertson et al	395	182.01
	1F	5,640,343	6/17/97	Gallagher et al	365	171
	1G	5,519,629	5/21/96	Snider	364	490
	1H					
	1I					
	1J					
	1K					

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	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

WSL	1R	J.R. Heath et al, "A Defect-Tolerant Computer Architecture: Opportunities for Nanotechnology", Science, Vol. 280, pp. 1716-1721 (June 12, 1998)
	1S	L. Guo et al, "Nanoscale Silicon Field Effect Transistors Fabricated Using Imprint Lithography", Applied Physics Letters, Vol. 71, pp. 1881-1883 (September 29, 1997)
	1T	A.M. Morales et al, "A Laser Ablation Method For The Synthesis Of Crystalline Semiconductor Nanowires", Science, Vol. 279, pp. 208-268 (January 9, 1998)

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U.S. PATENT DOCUMENTS

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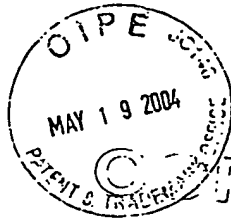
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

WBL	2R	J.R. Heath et al, "A Liquid Solution Synthesis Of Single Crystal Germanium Quantum Wires", Chemical Physics Letters, Vol. 208, No. 3, 4, pp. 263-268 (June 11, 1993)
↓	2S	V.P. Menon et al, "Fabrication and Evaluation Of Nanoelectrode Ensembles", Analytical Chemistry, Vol. 67, pp. 1920-1928 (July 1, 1995)
↓	2T	L. Guo et al, "A Silicon Single-Electron Transistor Memory Operating At Room Temperature", Science, Vol. 275, pp. 649-651 (January 31, 1997)

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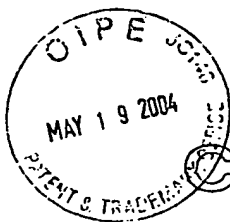
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

WSE	3R	S.J. Tans et al, "Room-Temperature Transistor Based On A Single Carbon Nanotube", Nature, Vol. 393, pp. 49-52 (May 7, 1998)
↓	3S	K.K. Likharev, "Correlated Discrete Transfer Of Single Electrons In Ultrasmall Tunnel Junctions", IBM Journal of Research and Development, Vol. 32, No. 1, pp. 144-158 (January 1998)
↓	3T	R.E. Jones Jr., et al, "Ferroelectric Non-Volatile Memories For Low-Voltage, Low-Power Applications", Thin Solid Films, Vol. 270, pp. 584-588 (December 1, 1995)

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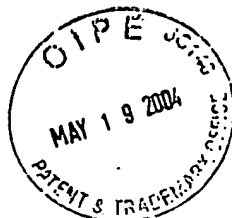
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	4P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

WSL	4R	D.B. Amabilino et al, "Aggregation Of Self-Assembling Branched [n]-Rotaxanes", New Journal of Chemistry, Vol. 22, No. 9, pp. 959-972 (September 11, 1998)
↓	4S	T. Vossmeier et al, "Combinatorial Approaches Toward Patterning Nanocrystals", Journal of Applied Physics, Vol. 84, No. 7, pp. 3664-3670 (October 1, 1998)
↓	4T	D.V. Leff et al, "Thermodynamic Control Of Gold Nanocrystal Size: Experiment And Theory", The Journal of Physical Chemistry, Vol. 99, pp. 7036-7041 (May 4, 1995)

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	5L							
	5M							
	5N							
	5O							
	5P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

WSL	5R	J.D.L. Holloway et al, "Electron-Transfer Reactions Of Metallocenes: Influence Of Metal Oxidation State On Structure And Reactivity", Journal of the American Chemical Society, Vol. 101, pp. 2038-2044 (April 11, 1979)
	5S	C. Mead et al, "Introduction to VLSI Systems", Addison-Wesley, Ch. 3, Section 10, pp. 79-82 (1980)
↓	5T	C.P. Collier et al, "Electronically Configurable Molecular-Based Logic Gates", Science, Vol. 285, pp. 391-394 (July 16, 1999)

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